

## Magnetic Sensor ICs

# X-axis Omnipolar Detection High Performance Hall-Effect Sensor ICs



## AS1086

### ● General Description

Using BCD process, the AS1086 is designed for high performance X-axis Omnipolar detection hall-effect application, such as automotive, industrial, electric tools, home appliances, rotor position sensing, brushless DC motor etc. The hall IC integrated an on-chip hall voltage generator for magnetic sensing, a comparator that amplifiers the hall voltage, hall sensor with dynamic offset cancellation system, an pull-up resistance, and a Schmitt trigger to provide switching hysteresis for noise rejection, and a voltage regulator for operation with supply voltage of 2.7V to 40V.

AS1086 is designed to respond to alternating North and South poles. When the magnetic flux density (B) is larger than operate point (B<sub>OP</sub>), the output will be turned on (High), the output is held until the magnetic flux density (B) is lower than release point (B<sub>RP</sub>), then turn off (low).

The device is available in Side Chip TFOT-4L Package and is rated over the -40°C to 125°C. The all packages are RoHS and Green compliant.

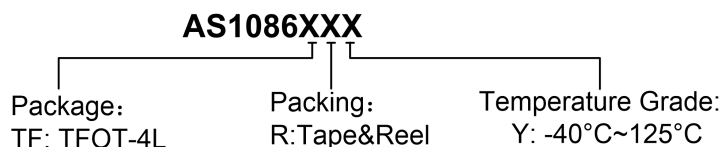
### ● Features

- X-axis Magnetic Flux Omnipolar Switch
- Omnipolar Operation, easy to use as output
- Input Voltage Range : 2.7V to 40V
- Very high sensitivity hall sensor
- Chopper stabilization amplifier stage
- High Magnetic Sensitivity
  - ✓ X-axis: B<sub>OP</sub>=±50Gauss, B<sub>RP</sub>=±30Gauss
- Good RF noise immunity
- PNP Open-Collector Output
- Small Solution Size
- RoHS & Green Compliant
- Side Chip TFOT-4L Package
- -40°C to +125 °C Temperature Range

### ● Applications

- Smart Meter
- Replace the Reed Switch
- Contact-less switch
- Air Cylinder, Antitheft window, Digital door lock
- Water Purifier, Humidifier Bidet
- Water Meter, Gas Meter, Wattmeter etc
- Washing Machine, Rice Cooker, Refrigerator, Electronic Dictionary, Digital Camera etc

### ■ Ordering Information



Part Number	B <sub>OP</sub> (Gauss)	B <sub>RP</sub> (Gauss)	Package Type	Package Qty	Temperature	Eco Plan
AS1086TFRY	±50	±30	TFOT-4L	7-in reel 3000pcs/reel	-40~125°C	Green

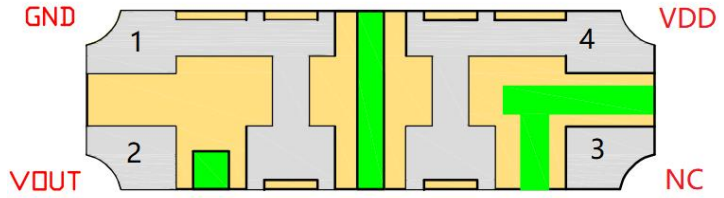
■ **Marking & Pin Assignment**

TFOT-4L:

Side View



Bottom View



Pin Name	Pin No.	I/O	Pin Function
	TFOT-4L		
GND	1	P	Ground
OUTPUT	2	O	Output Pin
NC	3	-	Not Connected
VDD	4	P	Input Power Supply

■ **Typical Application Circuit**

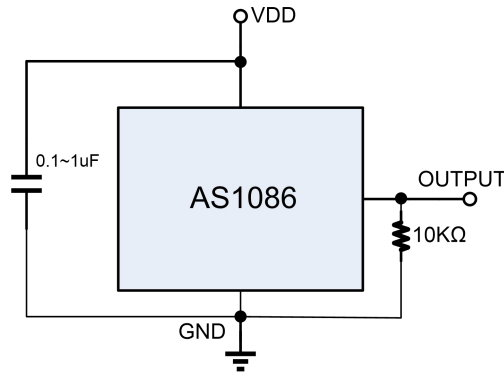


Figure 2, Typical Application Circuit of AS1086

■ **Block Diagram**

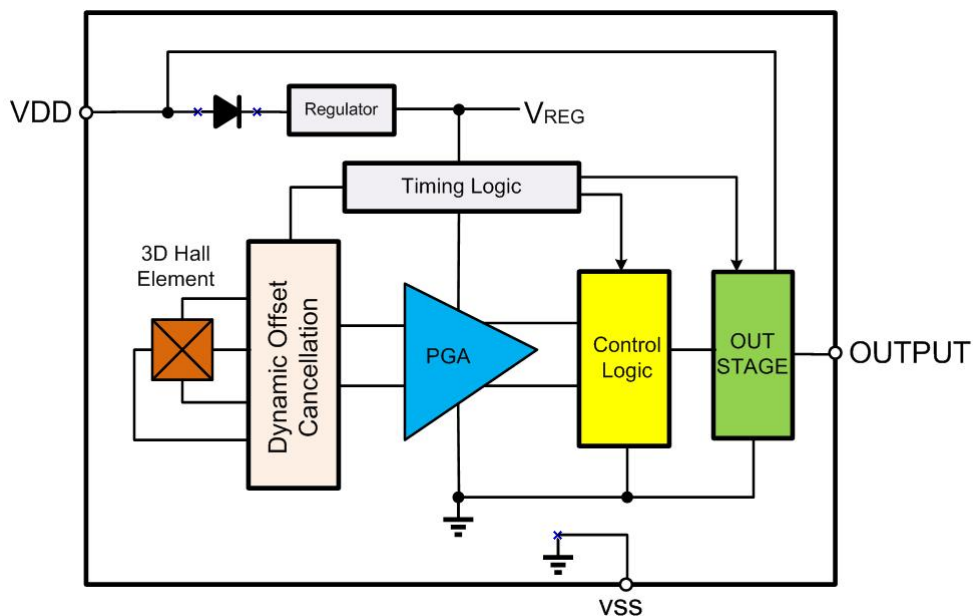


Figure 3, Block Diagram of AS1086

■ **Absolute Maximum Ratings<sup>1</sup>** ( $T_A=25^{\circ}\text{C}$ , unless otherwise noted)

Parameter	Symbol	Rating	Unit
$V_{DD}$ Pin to GND	$V_{DD}$	-0.3 to 45	V
Output Pin to GND	$V_{OUTPUT}$	-0.3 to $V_{DD} + 0.3$	V
Max. Continuous Output Current	$I_{OUTMAX}$	30	mA
Package Power Dissipation	TFOT-4L $P_D$	280	mW
ESD (HBM)	ESD	2000	V
Operating Junction Temperature Range	$T_{OP}$	-40 to +150	$^{\circ}\text{C}$
Maximum Soldering Temperature (at leads, 10 sec)	$T_{LEAD}$	260	$^{\circ}\text{C}$

■ **Recommended Operating Conditions<sup>2</sup>**

Parameter	Symbol	Rating	Unit
$V_{DD}$ Pin to GND	$V_{DD}$	2.7 to 40	V
Continuous Output Current	$I_{OUT}$	30	mA
Operating Temperature Range	$T_{OP}$	-40 to +125	$^{\circ}\text{C}$

Note: 1: Stresses above those listed in absolute maximum ratings may cause permanent damage to the device. Functional operation at conditions other than the operating conditions specified is not implied. Only one absolute maximum rating should be applied at any one time. 2: The device is not guaranteed to function outside of its operating conditions.

■ **Electrical Characteristics**

( $T_A = -40$  to  $+85^{\circ}\text{C}$  unless otherwise noted. Typical values are at  $T_A = +25^{\circ}\text{C}$ ,  $V_{DD} = 12\text{V}$ ) (1mT=10Gauss)

Symbol	Parameter	Conditions	Min.	Typ.	Max.	Unit
$V_{DD}$	Input Voltage		2.7	12	40	V
$I_{DD}$	Supply Current	No Magnetic	-	4.0	-	mA
$t_{ON}$	Power On Time		-	35	50	$\mu\text{s}$
$I_{OFF}$	Off-state Leakage Current	Output Hi-Z	-	-	1.0	$\mu\text{A}$
$V_{CESAT1}$	Collector-Emitter Saturation Voltage	$I_C = -10\text{mA}$	-	-	-0.3	V
$V_{CESAT2}$		$I_C = -50\text{mA}$	-	-	-0.4	V
$T_R$	Output Rise Time	$R_L = 1\text{K}$ , $C_L = 50\text{pF}$	-	-	0.5	$\mu\text{s}$
$T_F$	Output Fall Time	$R_L = 1\text{K}$ , $C_L = 50\text{pF}$	-	-	0.2	$\mu\text{s}$
$T_D$	Output Delay Time	$B = B_{RP} - 100\text{Gs}$ to $B_{OP} + 100\text{Gs}$	-	15	25	$\mu\text{s}$

**Magnetic Para. (X-Axis)**

$B_{OPS}$	Magnetic Operating Point		40	50	60	Gauss
$B_{OPN}$			-60	-50	-40	
$B_{RPS}$	Magnetic Release Point		20	30	-	Gauss
$B_{RPN}$			-	-30	-20	
$B_{HYS}$			-	20	-	Gauss

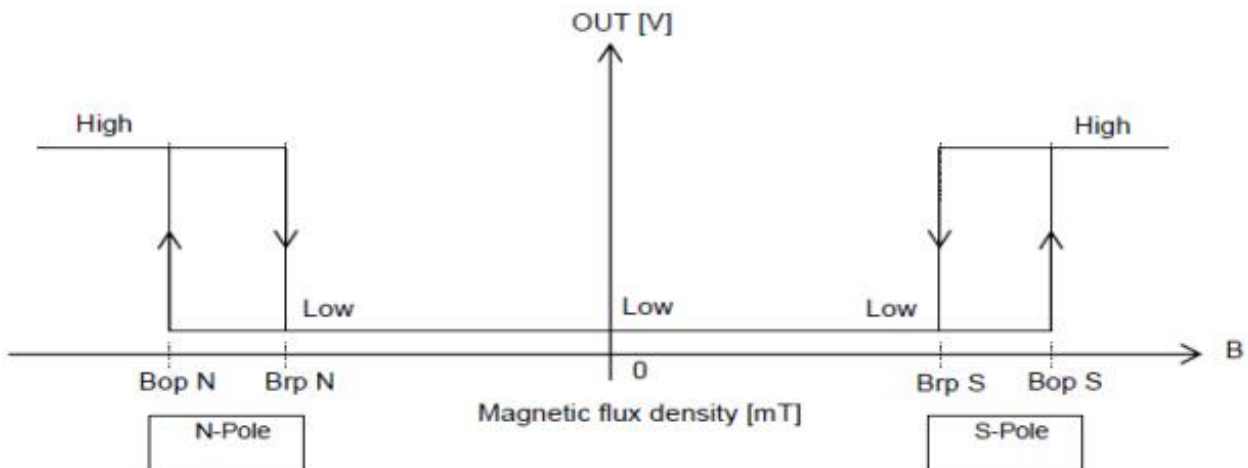


Fig 4, X-Axis Magnetic Operation Characteristic of AS1086

■ **Function Description**

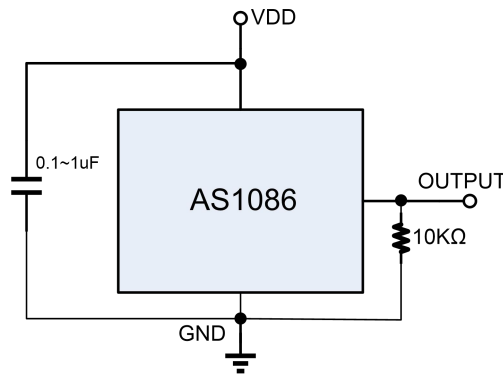
**Chopper-Stabilized Technique**

The Hall element can be considered as a resistor array similar to a Wheatstone bridge. A large portion of the offset is a result of the mismatching of these resistors. These devices use a proprietary dynamic offset cancellation technique, with an internal high-frequency clock to reduce the residual offset voltage of the Hall element that is normally caused by device over-molding, temperature dependencies, and thermal stress. The chopper-stabilizing technique cancels the mismatching of the resistor circuit by changing the direction of the current flowing through the Hall plate using CMOS switches and Hall voltage measurement taps, while maintains the Hall voltage signal that is induced by the external magnetic flux. The signal is then captured by a sample-and-hold circuit and further processed using low-offset bipolar circuitry. This technique produces devices that have an extremely stable quiescent Hall output voltage, are immune to thermal stress, and have precise recoverability after temperature cycling. A relatively high sampling frequency is used for faster signal processing capability can be processed.

**Operation**

The output of this device switches low (turns on) when a magnetic field perpendicular to the Hall sensor exceeds the operate point  $B_{OPS}$  (or is less than  $B_{OPN}$ ). After turn-on, the output is capable of sourcing up to 30mA and the output voltage is high  $V_{OUT(ON)}$ . When the magnetic field is reduced below the release point  $B_{RPS}$  (or increased above  $B_{RPN}$ ), the device output switches low (turns off). The difference between the magnetic operates and release points are the hysteresis ( $B_{HYS}$ ) of the device. This built-in hysteresis allows clean switching of the output even in the presence of external mechanical vibration and electrical noise.

■ **Applications:**



AS1086's pole-independent sensing technique allows for operation with either a north or south poles magnet orientation, enhancing the manufacturability of the device. The state-of-the-art technology provides the same output polarity for either pole face.

C1 serves two purposes: minimizing ripples on the input voltage and enhancing immunity from RF transmission noises within close proximity. Recommended values are between 100nF and 1uF. The larger the capacitance, the better the noise immunity is for the AS1086.

It is strongly recommended that an external bypass capacitor be connected (in close proximity to the Hall sensor) between the supply and ground of the device to reduce both external noise and noise generated by the chopper-stabilization technique. This is especially true due to the relatively high impedance of battery supplies. The simplest form of magnet that will operate these devices is a bar magnet with either pole near the branded surface of the device.

**Thermal Considerations**

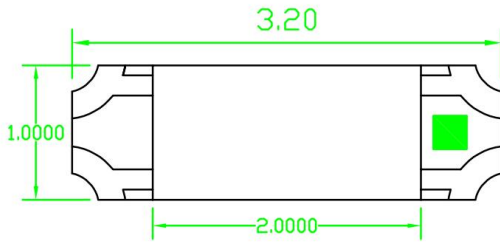
The maximum IC junction temperature should be restricted to 125°C under normal operating conditions. This restriction limits the power dissipation of the AS1086. Calculate the maximum allowable dissipation,  $P_{D(max)}$ , and keep the actual dissipation less than or equal to  $P_{D(max)}$ . The maximum-power-dissipation limit is determined using following equation:

$$P_{D(MAX)} = \frac{125^{\circ}C - T_A}{R_{\theta JA}}$$

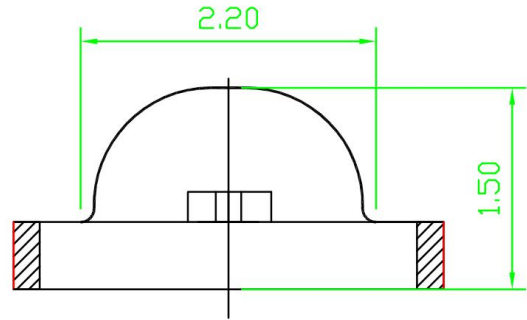
Where,  $T_A$  is the maximum ambient temperature for the application.  $R_{\theta JA}$  is the thermal resistance junction-to-ambient given in Power Dissipation Table.

■ **Package Information**

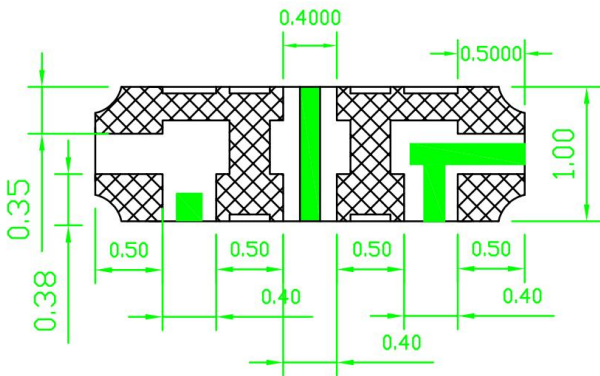
Side Chip TFOT-4L:



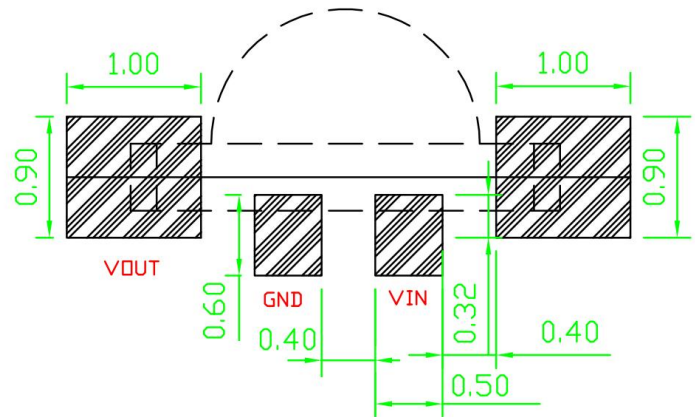
**TOP VIEW**



**SIDE VIEW**



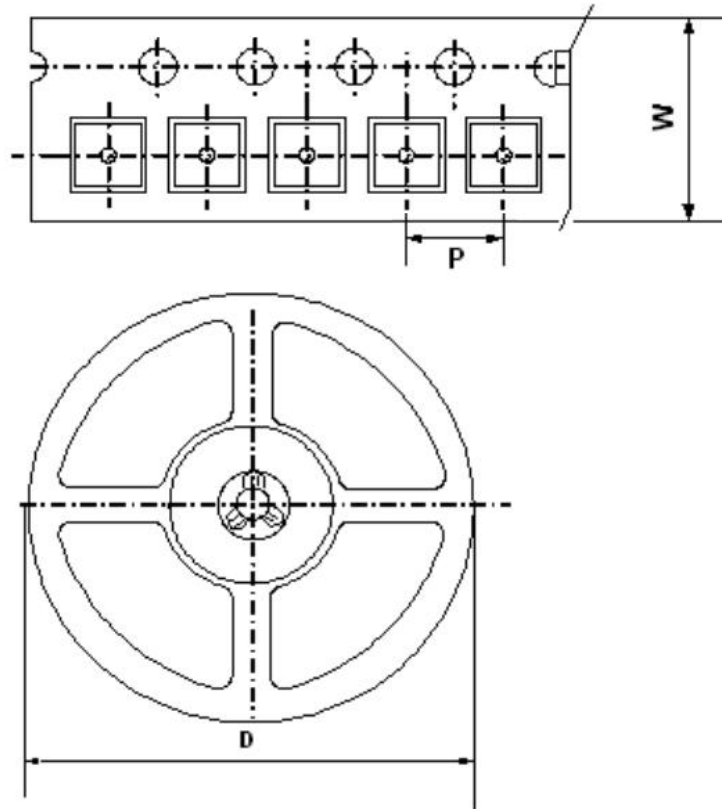
**BOTTOM VIEW**



**Layout Consideration**

■ Packing Information

TFOT-4L:



Package Type	Carrier Width(W)	Pitch(P)	Reel Size(D)	Packing Minimum
TFOT-4L	8.0±0.1 mm	4.0±0.1 mm	180±1 mm	3000pcs

Note: Carrier Tape Dimension, Reel Size and Packing Minimum